

Designing 2D And 3D Network-on-Chip Architectures

By Kostas Siozios;Dimitrios Soudris

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Design of 3D Optical Network on Chip Full Text Sign The proposed 3D mesh ONoC is compared with 2D counterpart in three aspects, i.e. energy,

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Design of 3D Optical Network on Chip Huaxi A 3D mesh based optical network on chip is developed together with a new It offers advantages over 2D design in

3d network-on-chip architectures using

The transition from 2D to 3D NoC architectures is done by was extended to designing custom 3D network for TSV-based 3D network on chip links, in

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Agenda | vlsi-soc 2007 conference in atlanta

High-Performance 3D FPGA Architectures, Kostas Siozios*, Kostas Sotiriadis* and Dimitrios Soudris of Network-on-Chip Interconnect, Dimitrios

Systematic design of 3d multicore architectures

SYStematic design of 3D Multicore Architectures with NeTwork - Prof. Dimitrios Soudris, Dr. Kostas Siozios (Dr. Kostas Siozios) 12:00 12:30 3D Integration

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Ieee xplore abstract - change function of 2d/ 3d

Change Function of 2D/3D Network-on-Chip By leveraging the existing technologies of computer networks, NoC enables the on-chip communication to be simpler

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Architecture and design of efficient 3d

Architecture and Design of Efficient 3D Network-on-Chip (3D NoC) for Custom Multicore SoC. the authors proposed a 2D-NoC named OASIS,

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Dimitrios Soudris, Network-on-Chip Architectures Kostas Siozios, Dimitrios Soudris, and Dionisios Pnevmatikatos

Citeseerx citation query 3d system integration

3d system integration technologies. Kostas Siozios, Dimitrios Soudris (3D) Network-on-Chip (NoC) architectures combine the benefits of new

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in NoC, in Designing 2D and 3D Network-on-Chip K. Siozios, I. Anagnostopoulos, D. Soudris, on 3D Network-on-Chip architectures,

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2D Hexagonal Mesh Vs 3D Mesh Network on Chip: exploration and performance evaluation of a new Network-on-Chip. Design, Automation & Test in Europe Conference &

Citeseerx design of 3-d optical network on chip

It offers advantages over 2D design in many aspects, A 3D mesh based optical network on chip is developed together with a new optical router architecture as the

W5 3d integration technology, architecture,

TechTuning technologies required to co-optimize process technology and chip design Kostas Siozios, Dimitrios Soudris for 3D Network-on-Chip,

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