

**Boolean Circuit Rewiring: Bridging Logical And
Physical Designs By Tak-Kei Lam;Xing Wei;Wai-
Chung Tang**

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The post-placement rewiring and rebuffering approach proposed (a circuit, a Boolean formula, a BDD, etc). EXAMPLE 1. Consider the multi-output function $z = x_1 \text{ XOR } y_1$

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the required logic. Karnaugh maps are used to simplify logic expressions in software design. Boolean bridging between the green

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Demonstrates techniques which will allow rewiring rates of over 95%, enabling adoption *Hitra in zanesljiva dostava, pla ilo tudi po povzetju.*

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